

# PATENT APPLICATION COVER SHEET

COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450



Sir/Madam:

Transmitted herewith is the patent application of:

Inventor(s): Joong S. Jeon and Huicai Zhong

For: "Method for Forming a Field Effect Transistor Having a High-K Gate Dielectric and Related Structure"

☐ This application is a ☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No.: \_\_\_\_\_

Enclosed are:

- ☒ Four (4) Sheets of drawings
- ☒ Request for Non-Publication (Form PTO/SB/35 (11-00))
- ☒ An assignment of the invention to Advanced Micro Devices, Inc.
- ☒ The enclosed payment includes \$40.00 for the recording of the assignment
- ☐ Applicant(s) claim(s) small entity status
- ☐ Information Disclosure Statement
- ☐ Preliminary Amendment
- ☒ Declaration and Power of Attorney
- ☒ The filing fee has been calculated as shown below:

	(Col. 1)	(Col. 2)
FOR:	No. Filed	No. Extra
BASIC FEE		
TOTAL CLAIMS	20 -20 =	0
INDEPENDENT CLAIMS	3 -3 =	0
MULTIPLE DEPENDENT CLAIMS PRESENTED		

If the difference in Col. 1 is less than zero, enter "0" in Col. 2

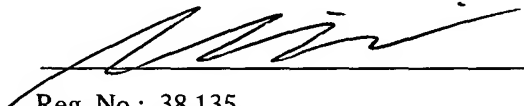
SMALL ENTITY	
RATE	FEE
	\$375.00
x 9 =	\$
x 42 =	\$
+140 =	\$
TOTAL	\$

OTHER THAN A SMALL ENTITY	
RATE	FEE
	\$750.00
x 18 =	\$0.00
x 84 =	\$0.00
+280 =	\$
TOTAL	\$750.00

- ☒ Enclosed is a payment in the amount of \$790.00 by credit card (Form PTO-2038 attached).
- ☐ Please charge Deposit Account No. 50-0731 in the amount of \$ \_\_\_\_.
- ☒ The Commissioner is hereby authorized to charge payment of any additional fees associated with this communication, or credit any overpayment to Deposit Account No. 50-0731. A duplicate copy of this sheet is enclosed.

MICHAEL FARJAMI, ESQ.

Date: 7/10/03

  
Reg. No.: 38,135



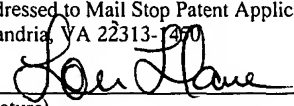
Michael Farjami, Esq.  
FARJAMI & FARJAMI LLP  
16148 Sand Canyon  
Irvine, CA 92618  
(949) 784-4600

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Date of Deposit 7/10/03

I hereby certify that this paper is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. § 1.10 on the date indicated above and is addressed to Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

  
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(Typed or Printed Name of Person Mailing Paper or Fee)

**NONPUBLICATION REQUEST  
UNDER  
35 U.S.C. 122(b)(2)(B)(i)**

First Named Inventor

Joong S. Jeon

Title

Method for Forming a Field Effect Transistor  
Having a High-K Gate Dielectric and Related  
Structure

Atty Docket Number

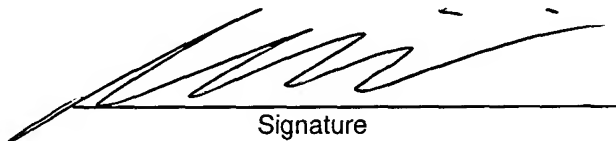
0180142

I hereby certify that the invention disclosed in the attached application **has not and will not be** the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing.

I hereby request that the attached application not be published under 35 U.S.C. 122(b).

7/10/03

Date



Signature

Michael Farjami, Esq.

Typed or printed name

This request must be signed in compliance with 37 CFR 1.33(b) and submitted with the application **upon filing**.

Applicant may rescind this nonpublication request at any time. If applicant rescinds a request that an application not be published under 35 U.S.C. 122(b), the application will be scheduled for publication at eighteen months from the earliest claimed filing date for which a benefit is claimed.

If applicant subsequently files an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications eighteen months after filing, the applicant **must** notify the United States Patent and Trademark Office of such filing within forty-five (45) days after the date of the filing of such foreign or international application. **Failure to do so will result in abandonment of this application (35 U.S.C. 122(b)(2)(B)(iii)).**